

ABSTRACT OF THE DISCLOSURE

- In a packet data processing apparatus for processing a packet received from a network by a processor, a packet data access part includes a
- 5 plurality of registers arranged in series and sequentially shifts the received packet through the plurality of registers toward an outlet of the packet data access part in synchronization with a clock.
- The processor processes the received packet by
- 10 accessing the packet data access part while the received packet is being shifting through the plurality of registers.

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